

SEP 22 2004
PATENT & TRADEMARK OFFICE

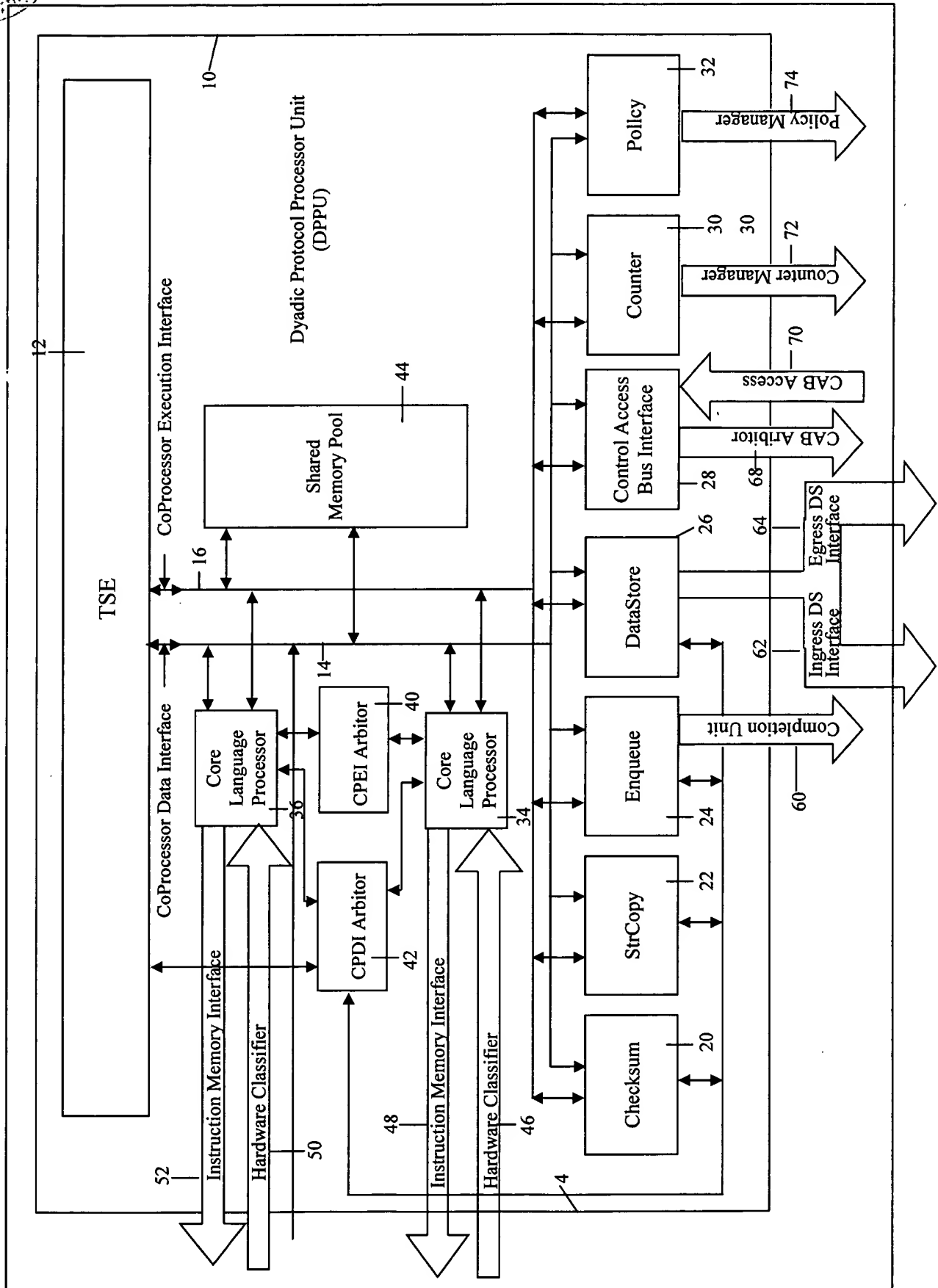


Figure 1

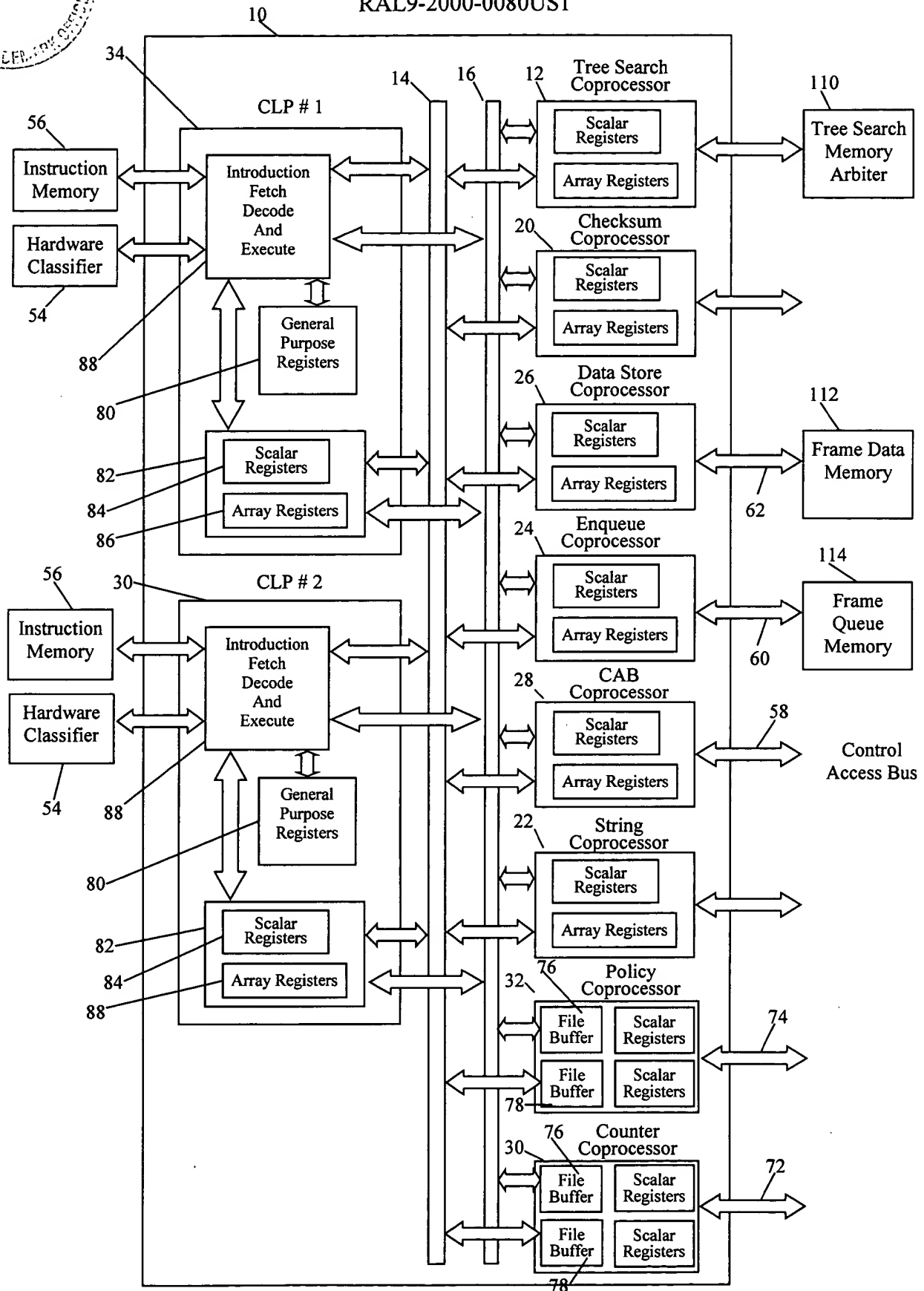


Figure 2

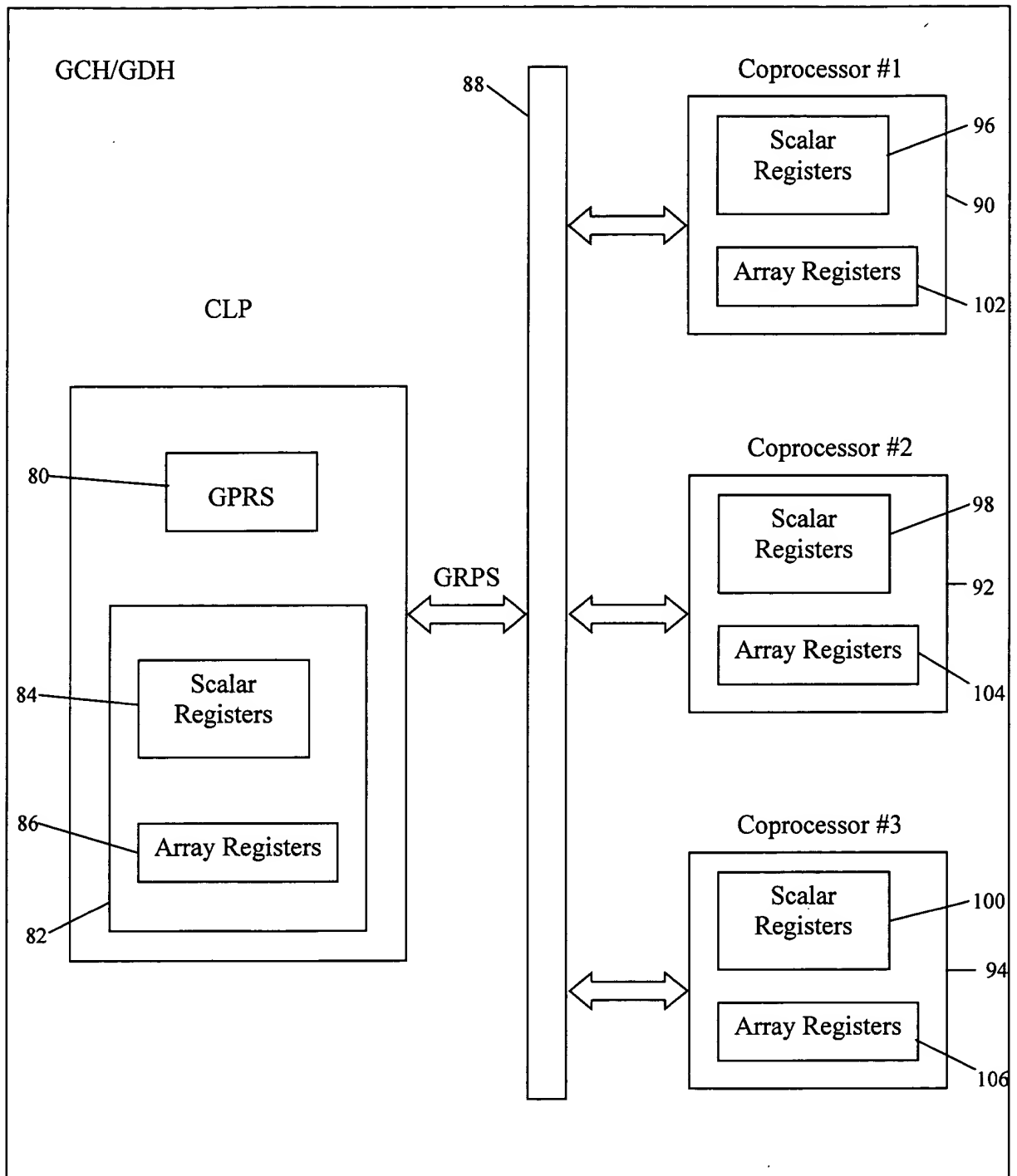


Figure 3

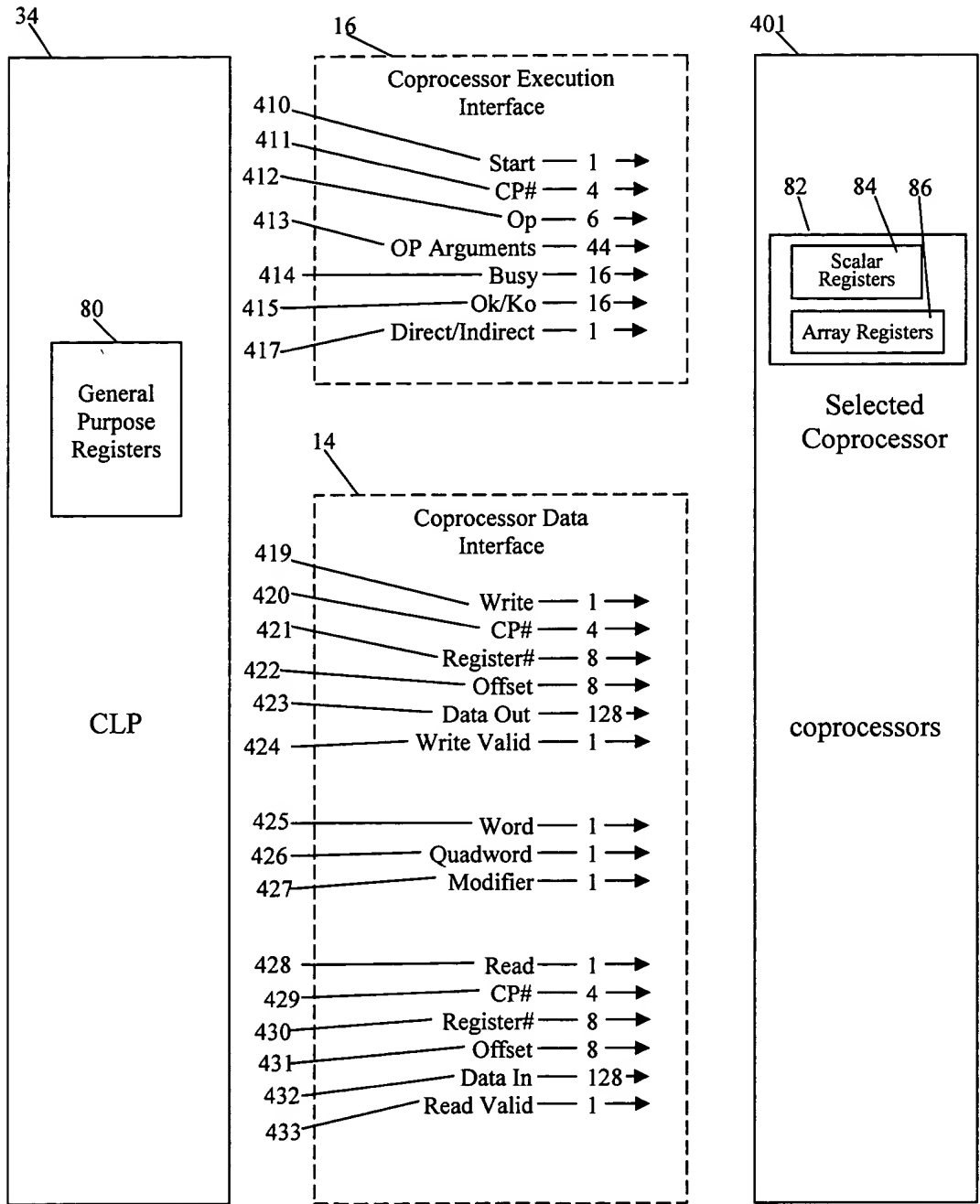
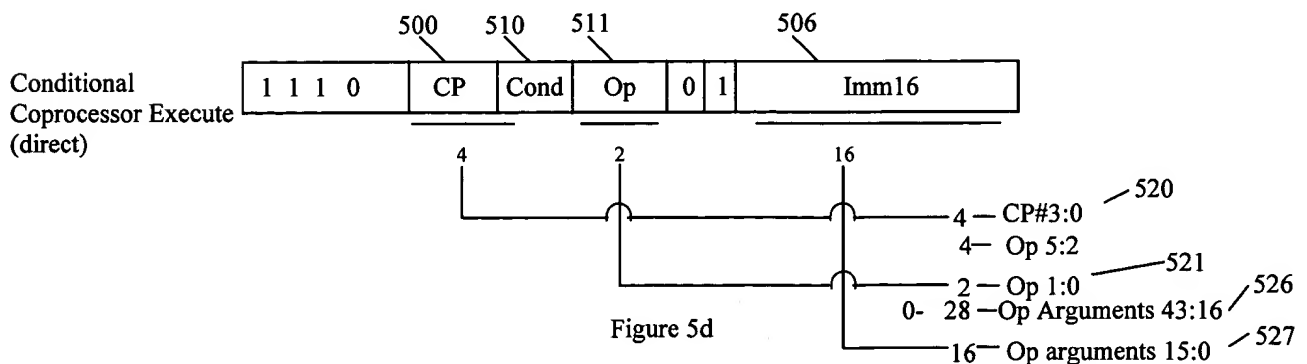
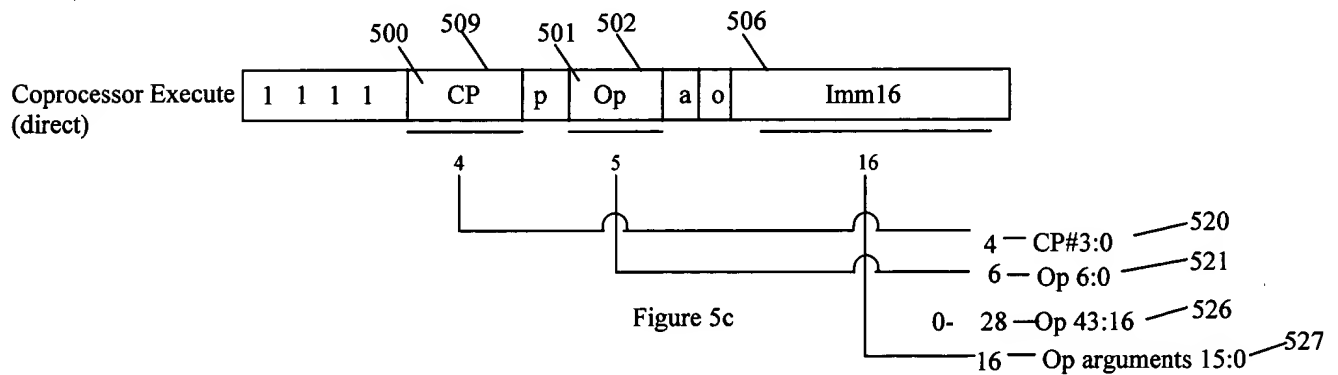
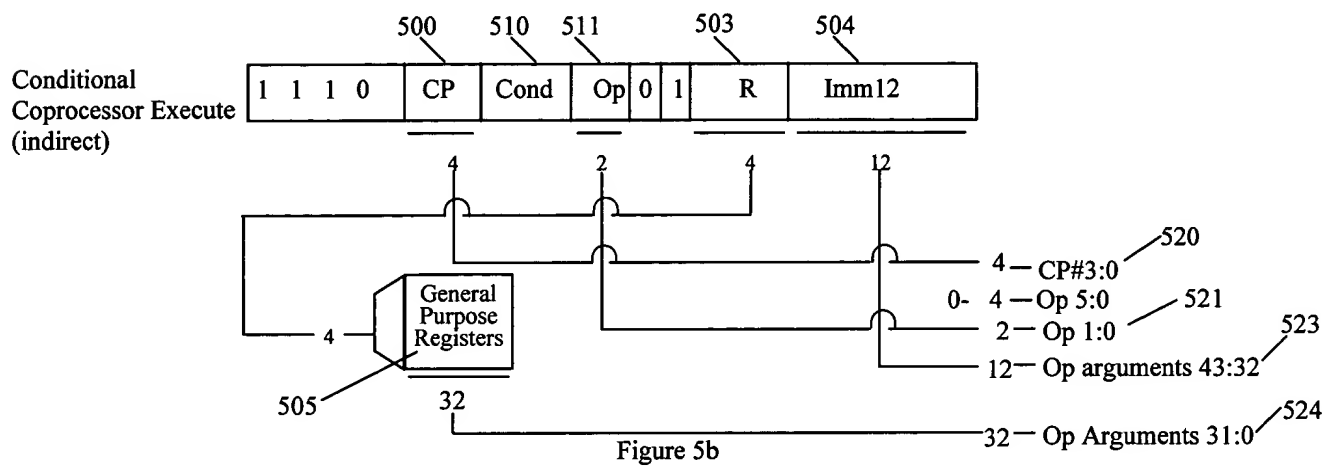
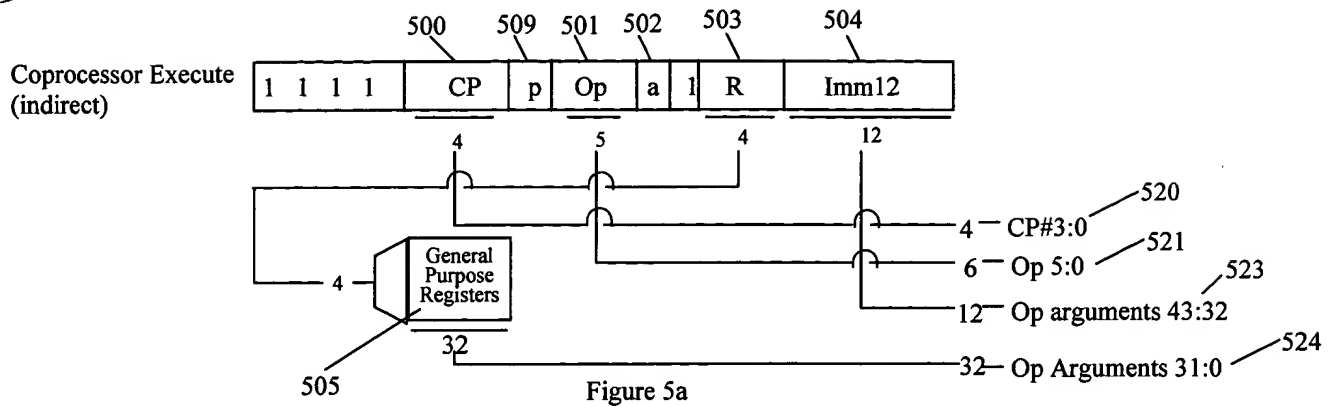


Figure 4



Sheet 5 of 6
Davis et al
RAL9-2000-0080US1



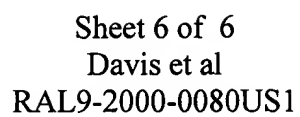


Figure 1 shows a 16-bit register structure. The register is divided into several fields: a 4-bit field '1110', a 4-bit field '0000', a 1-bit field 'p', a 7-bit field '0000000', and a 1-bit field '0'. A label '609' points to the 'p' field. A label '600' points to the '0' field. A label 'mask16' is placed below the '0' field.

	601	609		602		603
Coprocessor Wall and Branch	1 1 1 0	CP	p	0 0 0 0 0 0	0 k	1 target 16

Figure 6b